

EM210 Download Wince6.0 Image Manual

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1. The S5PV210 process of UART BOOT



As show the picture:

IROM CODE: is Samsung curing in the internal ROM a program, when the user choose to start from a serial port, this program segment will run, its role is to download the first level BOOT to internal 96K SRM in and run.

1st Boot loader:

After it run, initialize USB, and through the USB download 2nd Boot Loader, and run it. 2nd Boot Loader:

It is responsible for the management of the flash, download NK and other documents and burn wrote NAND inside.

OS:

WINCE OS BIN file.

From the above analysis we can know:

In S5PV210 IROM is Samsung curing internal, is the factory with good and cannot be change, and never lost. And 1st Boot loader and 2nd Boot Loader is running in RAM, does not keep them to NAND.

Serial port is used to guide from the first written burn blank NAND, called IROM guide.

2. The S5PV210 process of NAND BOOT

BOOTIMA	GE.NB0	тос	MBR	NK.BIN/CHAIN.LST	EXTENSION PARTITION
STEPLDR	EBOOT	EBOOT Configuration	Master Boot Record	os	Mounted at \NandFlash (RAM/ROM filesystem) \(ROM-only filesystem)
Block 0	Block 1 ~ Block 4	Block 5	Block 6 Sector 384	Single-XIP Multiple-XIP (BINFS)	Storage (exFAT)

From the above graph shows that:

Written in the burning SD (INAND) have two files, one is BOOTIAMGE. NB0, the other one is NK.Bin. One of the BOOTIMAGE.NB0 is STEPLDR and EBOOT connected



together a file Choose from SD (INAND) started, the S5PV210 will first loading STEPLDR, STEPLDR reload EBOOT, EBOOT reload NK.

From INAND FLASH start, it is called **normal guide**.

3. The compiled BSP generated several files introduction

BL_UART.nb0: Use IROM guide 1st Bootloader, only run in IRAM EBOOT.nb0: Use IROM guide 2nd Bootloader, only run in IRAM BootImage.nb0:

It is normal to guide the documents, the lead when IROM into two parts StepIdr + EBOOT, burned into NAND respectively

NK.bin: WINCE OS.

4. The IROM Serial guide to burn WINCE IMAGE

BOOT Select mode:

Boot mode	J1	J2	J3	J4	J5	J6
NAND	ON	ON	ON	ON	OFF	ON
SD(INAND)	ON	ON	OFF	OFF	ON	ON
USB	OFF	ON	ON	ON	OFF	ON

Note: The switch "JP106" up is OFF, down is ON.

Board default is SD (INAND) start-up mode.

If the board has burned the Linux or Android system, please format the nand flash before download the Wince Image. The format nand flash as show below:

(1) Format the SD card to FAT32 format.

(2) Open "moviNAND_Fusing_Tool_v2.0".



(3) Open the disk corresponds to the SD card in moviNAND_Fusing_Tool_v2.0 (please use the SD card provided by boardcon) and select the file you want to burn.



SD/MMC Drive G 💌	Drive Size 3813376 sectors SDHC Load Save
Size Configuration	Bootloader
SRAM Size 8 KB 💌	mage file Browse
EFuse Size 512 B 💌	The image file will be fused from to on drive
Partition Size	Kernel
Bootloader	Image file Browse
512 КВ 🛫	The image file will be fund from the 00 drive
Kernel	
4 MB 👻	Rootfs
Rootfs	Image file Browse
8 MB 💌	The image file will be fused from to on drive
Specific Sector	
Sector 0	Image File Browse
5UT1	

Click "**Browse**", that appear choose file dialog box, select the "**uboot**". The uboot is copied from Linux system or Android system. Here selecte the Linux system uboot in the CD: \ **EM210\Qt\Image\uboot**:



					? ×
查找范围(I):	20091229_5	D_FUSING_TOOL_EVT1	- 01	3 🗈 🖽-	
	sdfuse				
我最近的文档	u-boot				
桌面					
10					
我的文档					
我的电脑					
1					_
THE LOCKER	文件名 (2):			*	打开①
阿上邻居	计科学系 (4)。	Image (*. bin)			取消
阿上邻居	XH SER (D):	Party of the second			

(4)Click "START". The pop-up "NOTICE" window shows writing is successful.

D/MMC Drive	G 💌	Drive Size 3813376 sectors SDHC Load Save
iize Configurati	on	Bootloader
5RAM Size 8	KB 💌	Image file E:\210\20091229_SD_FU5ING_TOOL_EVT1\u-boot.bii Browse
EFuse Size 51	2 B 💌	The image file will be fused from to on drive
artition Size		Kernel
Bootloader		Image file Browse
512 K	в 👻	
Kernel		The image file will be fused from to on drive
4 N	18 👻	Rootfs
Rootfs		Image file Browse
8 N	1B 💌	The image file will be fused from to on drive
ipecific Sector		
Sector 0		Image File Browse



D/MMC Drive G	Drive Size 1990656 sectors SDHC Load Save
ize Configuration	Bootloader
SRAM Size 8 KB 🔽	Image file E:\guan\个人\super\婉写\u-boot.bin Browse
Fuse Size 512 B 💌	The image file will be fused from 49 to 1120 on drive G
artition Size	Kernel
Bootloader	Image file Browse
512 KB 🗸	
Kernel	The mage h Fusing image done to on drive
4 MB 💌	Rootfs 确定
Rootfs	Image file Browse
8 MB 🔽	The image file will be fused from to on drive
pecific Sector	
Sector 0	Image File Browse

(5) Insert the SD card, set the board to boot from SD, connect the Serial cable. Open SecureCRT and power on:

Serial-COMI - SecureCRT	×
File Edit View Options Transfer Script Tools Help	
x3 X3 C7 X1 X8 Pa R2 Q 73 55 🚭 27 XX 1 9 27	
Serial-COM1	×
U-Boot 1.3.4 (Dec 19 2011 – 11:02:30) for SMDKV210	^
CPU: S5PV210@1000MHz(OK) APLL = 1000MHz, HclkMsys = 200MHz, PclkMsys = 100MHz MPLL = 667MHz, EPLL = 80MHz HclkDsys = 166MHz, PclkDsys = 83MHz HclkPsys = 133MHz, PclkPsys = 66MHz SCLKA2M = 200MHz Serial = CLKUART Board: SMDKV210 DRAM: 512 MB Flash: 0 kB Flash: 0 kB SD/MMC: 121MB NAND: *maf_id = 236,dev_id = 218 256 MB The input address don't need a virtual-to-physical translation : 23e9d2a0 *** Warning - using default environment	
In: serial Out: serial	
Err: serial checking mode for fastboot Hit any key to stop autoboot: 2 🔳	~
Ready Serial: COM1 24, 34 24 Rows, 76 Cols VT100 NUM	

Click any key in 3 seconds countdown:

🖬 Serial-COII - SecureCRT	
File Edit View Options Transfer Script Tools Help	
123 第 第 8 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	
Serial-COM1	3
CPU: S5PV210@1000MHz(OK) APLL = 1000MHz, HclkMsys = 200MHz, PclkMsys = 100MHz MPLL = 667MHz, EPLL = 80MHz HclkDsys = 166MHz, PclkDsys = 83MHz HclkPsys = 133MHz, PclkDsys = 66MHz SCLKA2M = 200MHz Serial = CLKUART Board: SMDKV210	
DRAM: 512 MB Flash: 0 kB SD/MMC: 121MB NAND: "maf_id = 236,dev_id = 218 256 MB The input address don't need a virtual-to-physical translation : 23e9d2a0 *** Warning - using default environment	
In: serial Out: serial Err: serial checking mode for fastboot Hit any key to stop autoboot: 0 SMDKV210 # ∎	
Ready Serial: COM1 24, 12 24 Rows, 76 Cols VT100 NUM	.:

Then input the command: "nand scrub" to format the nand flash:

🕞 Serial-COII - SecureCRT	
File Edit View Options Transfer Script Tools Help	
43 X3 G- 43 X8 Pa R: Q G- F3 G- 12 X 1 9 23	
Serial-COM1	×
Board: SMDKV210 DRAM: 512 MB Flash: 0 kB SD/MMC: 121MB NAND: *maf_id = 236,dev_id = 218 256 MB The input address don't need a virtual-to-physical translation : 23e9d2a0 *** Warning - using default environment In: serial Out: serial	
Err: serial checking mode for fastboot Hit any key to stop autoboot: 0 SMDKV210 # nand scrub NAND scrub: device 0 whole chip warning: scrub option will erase all factory set bad blocks! There is no reliable way to recover them. Use this command only for testing purposes if you are sure of what you are doing!	
Really scrub this NAND flash? <y n=""> input y</y>	~
Keady Serial: CUMI 24, 1 24 Kows, 76 Cols VI100 N	JM



Serial-COII - SecureCRT	
File Edit View Options Transfer Script Tools Help	
41 X1 C7 41 X1 Pa (2 G (5 🖨 (2 / X1 ? 2 / Z	
Serial-COM1	×
There is no reliable way to recover them. Use this command only for testing purposes if you are sure of what you are doing!	^
Really scrub this NAND flash? <y n=""> Erasing at 0x3fe0000 25% complete. NAND 256MiB 3,3V 8-bit: MTD Erase failure: -5</y>	
NAND 256MiB 3,3V 8-bit: MTD Erase failure: -5 Erasing at 0x4500000 27% complete. NAND 256MiB 3,3V 8-bit: MTD Erase failure: -5 Erasing at 0x6660000 40% complete. NAND 256MiB 3,3V 8-bit: MTD Erase failure: -5 Erasing at 0x7d60000 49% complete. NAND 256MiB 3,3V 8-bit: MTD Erase failure: -5 Erasing at 0x91e0000 57% complete. NAND 256MiB 3,3V 8-bit: MTD Erase failure: -5	
Erasing at Oxbae0000 73% complete. NAND 256MiB 3,3V 8-bit: MTD Erase failure: -5 Erasing at Oxbfe0000 75% complete. NAND 256MiB 3,3V 8-bit: MTD Erase failure: -5 Erasing at Oxf5c0000 96% complete. NAND 256MiB 3,3V 8-bit: MTD Erase failure: -5 Erasing at Oxffe0000 100% complete. Scanning device for bad blocks OK SMDKV210 #	=
Ready Serial: COM1 27, 12 27 Rows, 76 Cols VT100	NUM

Or input the command: "nand erase" to format the nand flash:



(6) Download WINCE IMAGE: set the board to boot from USB, insert the USB cable,



power on the board, then open the "Dragin V1.26.2.exe" software, and set it as follow:

🦻 Dragin (V1.26.2)	
Basic Basic Option IROM Option Image: Contract of the second seco	Download Progress
	DEV1 DEV2 Log Log Reset All



🏶 Dragin (¥1.26.2)		×
Basic Basic Option	_ Download Pro	ogress
Step 1. IROM Boot BL1 d:\em210_wince\bl1_uart.nb0 TSR IMAGE d:\em210_wince\eboot.nb0	I	
Step 2. Select Images	L	TN
BOOT d:\em210_wince\bootimage.nb0	Ē	EM
OS d:\em210_wince\nk.bin	ŚY	P O I
Step 3. Detect Devices DETECT Image: Auto Detect DOWNLOAD Image: Auto Download	S GNUSMAS	Ë E D P A
	DEV1	DEV2
		Log
	Reset	

Then click" **DETECT**":

♣ Dragin (¥1.26.2)	×
Basic Option	Download Progress
Step 1. IROM Boot BL1 d:\em210_wince\bl1_uart.nb0	
TSR IMAGE d:\em210_wince\eboot.nb0	
	i T t N c E
OS d:\em210_wince\bootimage.nb0	t P D O
Step 3. Detect Devices	
DETECT Auto Detect Auto Reset	i P
DOWNLOAD Auto Download	
	DEV1 DEV2
	Reset All

Boardcon Embedded Design

Then press "Reset" button, and the image will be downloaded into the board:



Π. 1	Dragin	(971	96	9 Y -
	DIASIN '		20.	<i>41</i>

Boardcon Embedded Design

Basic Basic Option	-Download Progress
Step 1. IROM Boot BL1 d:\em210_wince\bl1_uart.nb0	
TSR IMAGE d:\em210_wince\eboot.nb0	
Step 2. Select Images	% T 9 N
B00T d:\em210_wince\bootimage.nb0	d E
OS d:\em210_wince\nk.bin	
Step 3. Detect Devices	N N N
DETECT Auto Detect Auto Reset	
DOWNLOAD 🔽 Auto Download	B A
Notify Connection(SUCCESS : 1)	
WARNING! : DON'T PLUG IT OUT UNTIL LABEL BECOME BLUE OR RED	DEV1 DEV2
	Reset All

Download NK.bin:



\triangleright	Boardcon Embedded Design
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Oragin (V1.26.2)		×
Basic Option	– Download Progress –	_
BL1 d:\em210_wince\bl1_uart.nb0		
TSR IMAGE d:\em210_wince\eboot.nb0		
Step 2. Select Images	S N	
BOOT d:\em210_wince\bootimage.nb0	d B	
OS d:\em210_wince\nk.bin		
Step 3. Detect Devices	N N N	
DETECT I Auto Detect I Auto Reset	S D P	
DOWNLOAD 🔽 Auto Download	Â	
WARNING! : DON'T PLUG IT OUT UNTIL LABEL BECOME BLUE OR RED		
Notify DOWNLOAD(TYPE : TSR, SUCCESS : 1)	DEV1 DEV2	ור
Notify DOWNLOAD(TYPE : BOOT, SUCCESS : 1)	Reset All	
,		



\triangleright	Boardcon Embedded Design
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Dragin (V1.26.2) Basic Option	
Step 1. IROM Boot downl BL1 d:\em210_wince\bl1_uart.nb0 about	oad nk.bin, need
TSR IMAGE d:\em210_wince\eboot.nb0	
Step 2. Select Images BOOT d:\em210_wince\bootimage.nb0 OS d:\em210_wince\nk.bin	T % N O E e M o E an D I I
Step 3. Detect Devices DETECT Auto Detect DOWNLOAD Auto Download	h V Sa E D F P A
WARNING! : DON'T PLUG IT OUT UNTIL LABEL BECOME BLUE OR RED Notify DOWNLOAD(TYPE : TSR, SUCCESS : 1) Notify DOWNLOAD(TYPE : BOOT, SUCCESS : 1)	DEV1 DEV2 Log Log Reset All

After download all image to the board, then close Dragin:

Dragin (V1.26.2)		
Basic Option Step 1. IROM Boot BL1 d:\em210_wince\bl1_uart.nb0 TSR IMAGE	Download Pro	ogress
Step 2. Select Images BOOT d:\em210_wince\bootimage.nb0 OS d:\em210_wince\nk.bin	L MET SY S	NE Pol
Step 3. Detect Devices DETECT Image: Auto Detect DOWNLOAD Image: Auto Download	GNUSHAS	Ŭ E D P A
	DEV1 Log Rese	DEV2 Log t All

Boardcon Embedded Design

(2) Set the board to boot from "SD (INAND)" pull out the USB cable, open the SecureCRT, repower on the board:

www.boardcon.com



G Serial-COLI - SecureCRT	
File Edit View Options Transfer Script Tools Help	
19 19 G 20 10 10 10 10 10 10 10 10 10 10 10 10 10	
Serial-COM1	X
<pre>dwversion: 0x1 dwsignature: 0x48534643 String: NK dwImageType: 0x8 dwTlSectors: 0x7EBE dwLoadAddress: 0x80020000 dwJumpAddress: 0x80027DCC dwstoreOffset: 0x0 } ID[4] {</pre>	~
<pre>dwversion: 0x0 dwSignature: 0x0 String: dwImageType: 0x8770E394 dwTtlSectors: 0x0 dwLoadAddress: 0x0 dwJumpAddress: 0x8770E50C dwStoreoffset: 0x0 } [Ebootl g_pBSPArgs->bDVFSDisable: 0</pre>	
[Eboot] ++InitializeDisplay()	
[Eboot]InitializeDisplay() Press [ENTER] to download now or [SPACE] to cancel.	-
Initiating image download in 4 seconds.	~
Ready Serial: COM1 27, 41 27 Rows, 76 Cols VT100	NUM

Click [SPACE], select the LCD size:

🕞 Serial-COII - SecureCRT	K
File Edit View Options Transfer Script Tools Help	
12 X L X X Pa C Q Z S A 2 N X I 8 2	
Serial-COM1	3
Initiating image download in 4 seconds. ************************************	
Enter Choice >> g	
<pre>1. WanXin WXCAT43 2. HannStar HSD050 3. INNOLUX AT070V83V.1 4. VGA(CH7026) 5. AT080TN52(800*600) 6. AT070TN92(800*480) Enter LCD Select: 6************************************</pre>	
** System Configuration **	
C) Bootloader Configurations N) NAND Flash Memory Menu L) Launch existing flash resident image now	
X: Exit to previous menu	
Enter Choice >>	~
Ready Serial: COM1 31, 18 31 Rows, 76 Cols VT100 NUM	



Then restart the board, the WINCE6.0 system is start, as follow:

🖬 Serial-COII - SecureCRT
File Edit View Options Transfer Script Tools Help
19 19 17 19 18 18 19 19 19 19 19 19 19 19 19 19 19 19 19
Serial-COM1
dwStoreOffset: 0x0
[Eboot] g_pBSPArgs->bDVFSDisable: 0 [Eboot] ++InitializeDisplay()
[Eboot]InitializeDisplay() Press [ENTER] to download now or [SPACE] to cancel.
Initiating image download in 0 seconds. EOTG: USB Init A OTG cable NOT inserted! OEMPlatformInit. System ready! Preparing for download INFO: g_ImageType: 0x8 OEMLaunch: IMAGE_TYPE_NK Launch the NK dwstartAddr = 0x80020000 dwLength = 0x4 NK image dwstartAddr = 0x80027dcc dwLength = 0x4 NK image
INC Image IsValidMBR: MBR sector = 0x180 (valid MBR) OpenPartition: Partition Exists=0x1 for part 0x21. BP_SetDataPointer at 0x0 IMAGE_EBOOT_FRAMEBUFFER_UA_START = 0xa7e00000, LCD_WIDTH = 800 LCD_HEIGHT=48
ReadData: Start = 0x0, Length = 0x3f5f000. Log2Phys: Logical 0x1 -> Physical 0x181 TotalSector is = 32446.
Ready Serial: COM1 31, 1 31 Rows, 76 Cols VT100 NUM



🖬 Serial-COII - SecureCRT
File Edit View Options Transfer Script Tools Help
\$P\$
Serial-COM1
PHY_SetPowerDown [MFC] DLL_PROCESS_ATTACH! MFC_Init()++++ MFCPowerInit() MfcFWBufMemMapping()+++++ [1][CMM_IOCONTrOl] IOCTL_CODEC_MEM_ALLOC [CMM_IOCONTrOl] IOCTL_CODEC_GET_PHY_ADDR(0xd3240000) MfcFWBufMemMapping() InitializeIST() MFC_INIt() MFC_power on [UFNPDD] USB_POWER : D0 IN [UFNPDD] USB_POWER : D0 IN [UFNPDD] USB_POWER : D0 MFC power off OUT [UFNPDD] USB_POWER : D0 IN [UFNPDD] USB_POWER : D0 MFC power off OUT [UFNPDD] USB_Serial Function Class Enabled LCD type=6, LCD width=800, LCD high= 480 +KeybdDriverInitializeAddresses LayMgr.cpp: Layout Manager successfully initialized to 2 Maximum Allowed Error 7: Backlight driver: GetSystemPowerStstusEx2 failed with error 0x1f. [BootCompleteApp] Boot Complete Event occured [UFNPDD] USB_POWER : D0 IN [UFNPDD] USB_POWER : D0 OUT MSIM: IM_ReadRegistry read KB 5 Explorer(V2.0) taskbar thread started. NDISPWR:: Found adapter [DM9CE1] NDISPWR:: Found adapter [WWANL]
Ready Serial: COM1 31, 1 31 Rows, 76 Cols VT100 NUM