EM2416 Hardware Manual

Boardcon Technology Limited
www.boardcon.com

1. Introduction

1.1. About this Manual

This manual is intended to provide the user with an overview of the board and benefits, complete features specifications, and set up procedures. It contains important safety information as well.

1.2. Feedback and Update to this Manual

To help our customers make the most of our products, we are continually making additional and updated resources available on the Boardcon website (www.boardcon.com , www.armdesigner.com).

These include manuals, application notes, programming examples, and updated software and hardware. Check in periodically to see what's new!

When we are prioritizing work on these updated resources, feedback from customers is the number one influence, If you have questions, comments, or concerns about your product or project, please no hesitate to contact us at support@armdesigner.com.

1.3. Limited Warranty

Boardcon warrants this product to be free of defects in material and workmanship for a period of one year from date of buy. During this warranty period Boardcon will repair or replace the defective unit in accordance with the following process:

A copy of the original invoice must be included when returning the defective unit to Boardcon. This limited warranty does not cover damages resulting from lighting or other power surges, misuse, abuse, abnormal conditions of operation, or attempts to alter or modify the function of the product.

This warranty is limited to the repair or replacement of the defective unit .In no event shall Boardcon be liable or responsible for any loss or damages, including but not limited to any lost profits, incidental or consequential damages, loss of business, or anticipatory profits arising from the use or inability to use this products.

Repairs make after the expiration of the warranty period are subject to a repair charge and the cost of return shipping. Please contact Boardcon to arrange for any repair service and to obtain repair charge information.

Content

1 EM2416 Introduction

1.1 Summary

EM2416 single board computer (SBC) is powered by Samsung ARM9 S3C2416X @400MHz. The SBC offers true rapid development solutions by providing all the necessary ingredients to jump start embedded designs. The board includes the MINI2416-III SOM and Carrier Board (customized embedded motherboard), pre-installed Linux or WinCE6.0 OS.

1.2 S3C2416 Features

The S3C2416X offers outstanding features with its CPU core, a 16/32-bit ARM926EJ RISC processor designed.

Features:

• Around 400MHz @ 1.2V Core, 1.8V/2.5V/3.0V/3.3V ROM/SRAM, 1.8V/2.5V mSDR/mDDR/DDR2 SDRAM,

1.8V/2.5V/3.3V external I/O microprocessor with 16KB I/D-Cache/MMU

• External memory controller (mSDR/mDDR/DDR2 SDRAM Control and Chip Select logic)

• LCD controller (up to 256K color TFT) with LCD-dedicated DMA

• 6-ch DMA controllers with external request pins

• 4-ch UARTs (IrDA1.0, 64-Byte Tx FIFO, and 64-Byte Rx FIFO)

• 1-ch High Speed SPIs

• 1 IIC bus interfaces (multi-master support)

• 1 IIS Audio CODEC interfaces (24-bit, port 0 supports 5.1ch)

• AC97/PCM CODEC Interface(muxed with I2S)

• 2 High-Speed MMC and SDMMC combo (SD Host2.0 and MMC protocol 4.2 compatible)

• 2-ch USB Host controller (ver 1.1 Complaint)/1-ch USB Device controller (ver 2.0 Complaint)

• 4-ch PWM timers / 1-ch Internal timer / Watch Dog Timer

• 10-ch 12-bit ADC and Touch screen interface

• RTC with calendar function

• 138 General Purpose I/O ports / 16-ch external interrupt source

• Power control: Normal, Idle, Stop, Deep Stop and Sleep mode

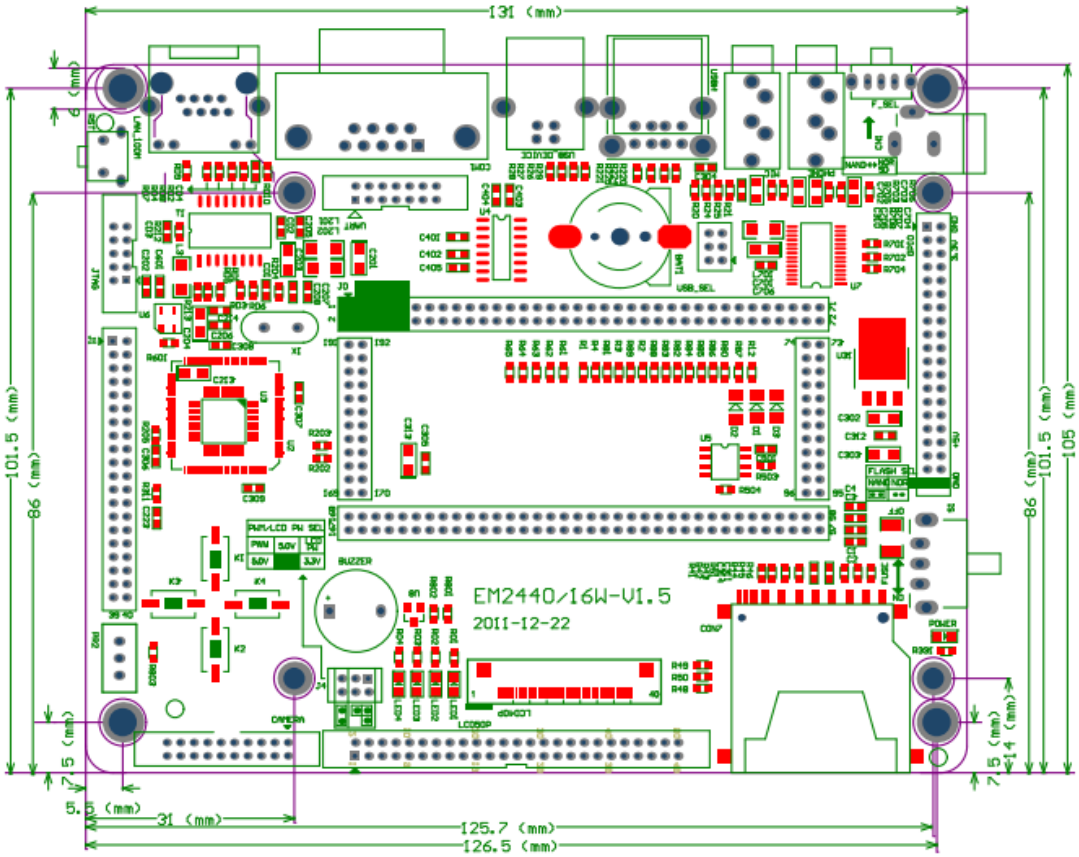
• On-chip clock generator with PLL

1.3 EM2416 Features

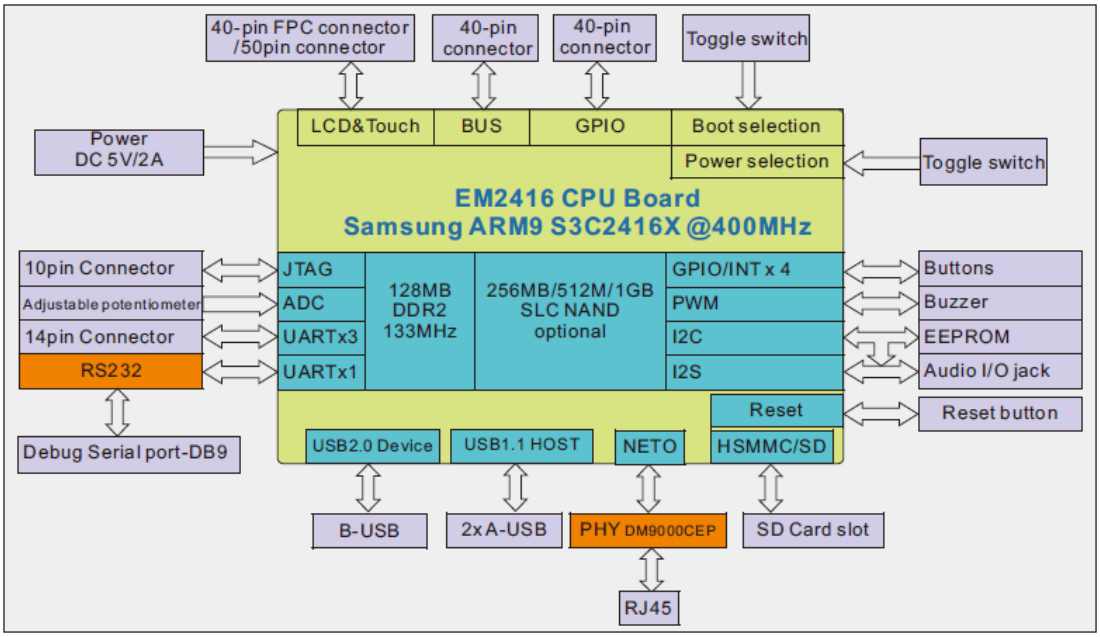
Feature	Specifications
CPU	Samsung ARM9 S3C2416X 400MHz with separate 16KB instruction and 16KB data caches, 2D Accelerator
SDRAM	128MB

NAND Flash	256MB/512MB/1GB optional
CPU Power	3.3V power supply for CPU board
Connector	DC-2.0mm pitch board-to-board connectors
CPU board dimension	37mm x 74mm
Serial Ports (UARTs)	COM0 - RS232, five-wire DB9 RS232 serial port COM1/2/3, three-wire available via 2mm pitch 3 pins
LCD	The LCD port integrated 4-wire resistor touch screen port. The board comes with driver for 4.3" and 7" TFT LCD (the resolution is 800*600)
Ethernet	Davicom DM9000CEP & PHY, 10/100BaseT, Activity LED's
USB	2x USB1.1 Host, 1x USB2.0 Device
JTAG	1x 2mm pitch 10 pins JTAG Port
Keyboard & mouse	USB, keypad is working via USB
Audio codec	Adopt WM8731/UDA1341 Audio chip, Audio input and output slot
ADC	Adjustable resistor is connected with pins of ADC to check analog/digital change
RTC	Real Time Clock, powered by external lithium battery
Buzzer	On-board PWM function test unit
Device support	1x SD/MMC card socket, Supports Multimedia Card, Secure Digital and Secure Digital I/O communications protocols up to 2GB
GPIO	1x 40-pin GPIO Expansion Connector
BUS	1x 40-pin BUS extended interface
LED	4x Status LED
Buttons	4x Programmable User Buttons
Power in	DC 5V/2A
Base board dimension	105mm x 131mm

1.4 PCB Dimension



1.5 Block Diagram



1.6 Motherboard Power meter

Support voltage	5v/2A				
System	Connected devices	Electric current(A)	System	Connected devices	Electric current(A)
Linux	5v power	0.145	Linux	Power, 7 inch resistive screen	0.790
Linux	Power , sd card, play video, U disk, debug serial, Ethernet, 7inch LCD, headphone	1.021	Wince	5v power	0.311
Wince	Power, 7 inch resistive screen	0.940	Wince	Power , sd card, play video, U disk, debug serial, Ethernet, 7inch LCD, headphone	0.964

1.7 CPU Module Introduction

The EM2416 CPU board – MINI2416-III is designed specifically for business users who develop consumer electronics, industrial control, vehicle navigation, PDA and other electronic products.

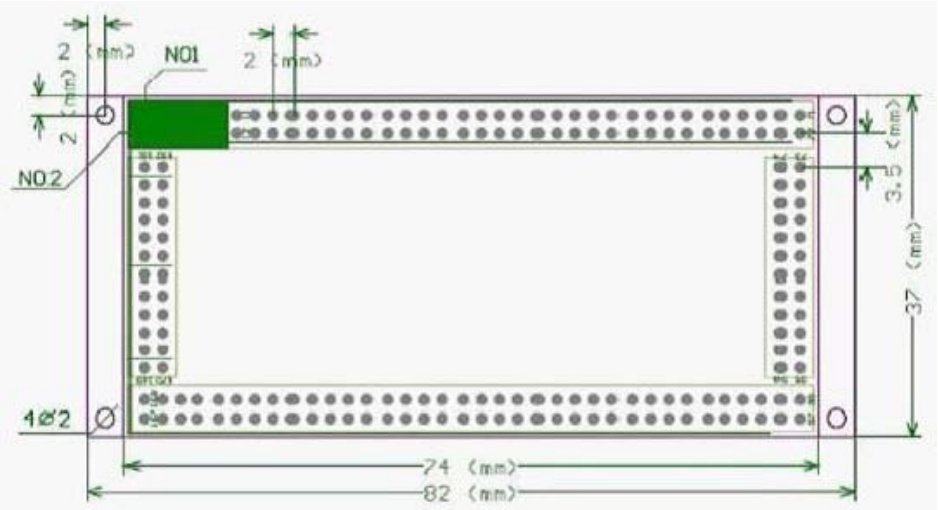
Board Dimension

* Board size: 74mm x 37mm

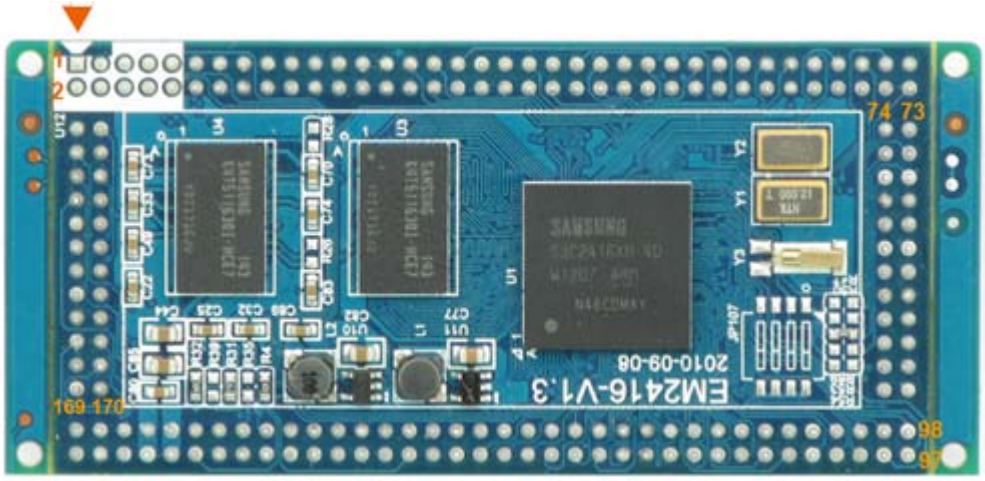
* Pin to Pin space: 2mm

* Pin number: (J1A +J1C) x 24 + (J1B+J1D) x 72, total 192 pins

MINI24116-III PCB Dimension



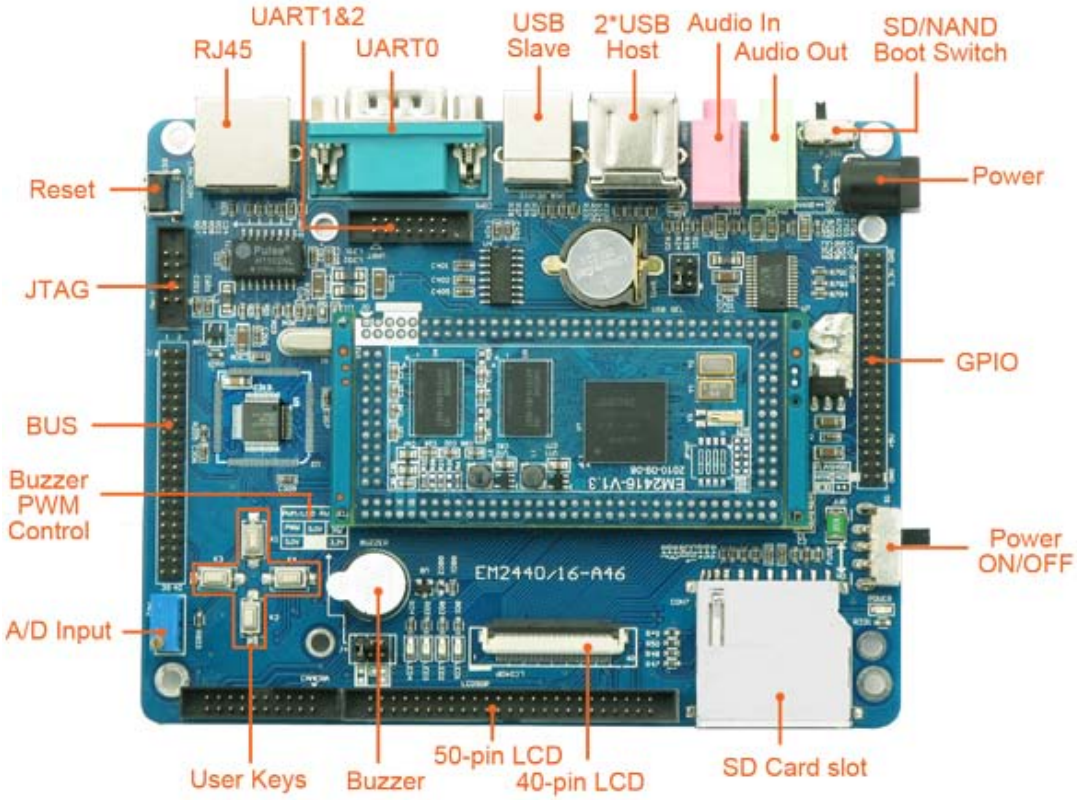
Pin Definition



Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
1	DATA6	2	DATA7	3	ADDR7	4	ADDR8
5	ADDR5	6	ADDR6	7	ADDR3	8	ADDR4
9	ADDR1	10	ADDR2	11	DATA30	12	DATA31
13	DATA28	14	DATA29	15	DATA26	16	DATA27
17	DATA24	18	DATA25	19	DATA22	20	DATA23
21	DATA20	22	DATA21	23	DATA18	24	DATA19
25	DATA16	26	DATA17	27	nTRST	28	nRESRT
29	TDO	30	TDI	31	TCK	32	TMS
33	RXD2/GPH5	34	TXD2/GPH4	35	RXD1/GPH3	36	TXD1/GPH2
37	RXD0/GPH1	38	TXD0/GPH0	39	nRTS0/GPH9	40	nCTS0/GPH8
41	nRTS1/GPH11	42	nCTS1/GPH10	43	RXD3/GPH7/nCTS2	44	TXD3/GPH6/nRTS2
45	EINT4/GPF4	46	EINT5/GPF5	47	EINT6/GPF6	48	EINT7/GPF7
49	EINT8/GPG0	50	EINT11/GPG3	51	EINT14/GPG6	52	EINT13/GPG5
53	EINT15/GPG7	54	EINT12/GPG4	55	EINT10/GPG2	56	EINT9/GPG1

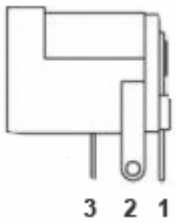
57	EINT2/GPF2	58	VDD_RTC	59	DP_UDEV	60	AIN3
61	DM_UDEV	62	AIN2	63	DN	64	AIN1
65	DP	66	AIN0	67	SPIMISO/GPE11	68	SS/GPL13
69	SPICLK/GPE13	70	SPIMOSI/GPE12	71	EINT0/GPF0	72	nXBREQ/GPB6/RTCK
73	CLKOUT1/GPH14	74	RGB_LEND/GPC0	75	RADDR21/GPA6	76	RADDR22/GPA7
77	RADDR23/GPA8/BOOT_SEL	78	RADDR24/GPA9	79	EINT3/GPF3	80	SD1_DAT2/GPL2
81	SD1_DAT3/GPL3	82	SD1_CMD/GPL8	83	SD1_CLK/GPL9	84	SD1_DAT0/GPL0
85	SD1_DAT1/GPL1	86	TCLK/GPB4	87	RSMCLK/GPA23	88	I2SCDCLK/GPE2
89	I2SLRCK/GPE0	90	I2SSCLK/GPE1	91	RSMVAD/GPA24	92	RSMBWAIT/GPM0
93	I2SSDI/ GPE3	94	I2SSDO/ GPE4	95	nXBACK/GPB5	96	XP/AIN9
97	XM/AIN8	98	YP/AIN7	99	YM/AIN6	100	RGB_VCLK/GPC1
101	VLINE/GPC2	102	VFRAME/GPC3	103	VM/GPC4	104	IICSCL/GPE14
105	IICSDA/GPE15	106	VD23/GPD15	107	VD22/GPD14	108	VD21/ GPD13
109	VD20/ GPD12	110	VD19/GPD11	111	VD18/GPD10	112	VD17/GPD9
113	VD16/GPD8	114	VD15/GPD7	115	VD14/GPD6	116	VD13/ GPD5
117	VD12/GPD4	118	VD11/GPD3	119	VD10/GPD2	120	VD9/GPD1
121	VD8/GPD0	122	VD7/GPC15	123	VD6/GPC14	124	VD5/GPC13
125	VD4/GPC12	126	VD3/GPC11	127	VD2/GPC10	128	VD1/GPC9
129	VD0/GPC8	130	nXDACK/GPB9	131	nXDREQ/GPB10	132	AIN4
133	AIN5	134	TOUT3/GPB3	135	TOUT2/GPB2	136	TOUT1/GPB1
137	TOUT0/GPB0	138	nRSTOUT	139	nBATT_FLT	140	nRBE0
141	nRBE1	142	CLKOUT0/GPH13	143	EINT1/GPF1	144	SD0_DAT2/GPE9
145	SD0_DAT3/GPE10	146	SD0_CMD/GPE6	147	SD0_CLK/GPE5	148	SD0_DAT0/GPE7
149	SD0_DAT1/GPE8	150	UARTCLK/GPH12	151	nWAIT	152	nRCS1/GPA12
153	nRCS2/GPA13	154	nRCS3/GPA14	155	nRCS4/GPA15	156	nRCS5/GPA16
157	nRCS0	158	NC	159	GND	160	GND
161	3.3V	162	3.3V	163	DATA8	164	DATA9
165	DATA10	166	DATA11	167	DATA12	168	DATA13
169	DATA14	170	DATA15	171	ADDR25/GPA10	172	ADDR0/GPA0
173	nRWE	174	nROE	175	ADDR20/GPA5	176	ADDR19/GPA4
177	ADDR18/GPA3	178	ADDR17/GPA2	179	ADDR16/GPA1	180	ADDR15
181	ADDR14	182	ADDR13	183	ADDR12	184	ADDR11
185	ADDR10	186	ADDR9	187	DATA0	188	DATA1
189	DATA2	190	DATA3	191	DATA4	192	DATA5

2 Peripherals Introduction



2.1 Power (CN1)

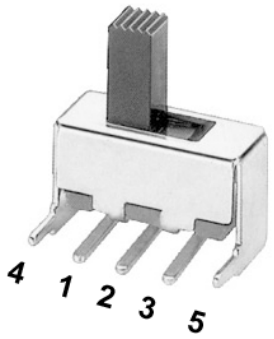
Power supply: DC 5V/2A (The Input voltage must be less than 7V).



Pin	Signal	Description	Pin	Signal	Description
1	VDD5V	Main power supply. DC 5V power in, connect to SW-SPDT	2	GND	Ground
3	GND	Ground			

2.2 Power switch (POWER)

The power switch is a toggle switch, controlling the evaluation board power ON/OFF.

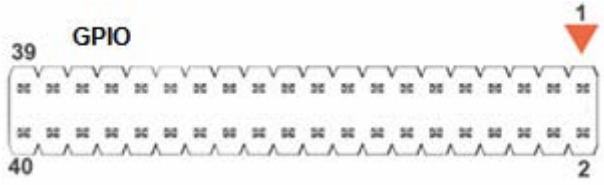


Pin	Signal	Description	Pin	Signal	Description
1	Connect to FUSE	SW SPDT switch 1 and 2, power to board	2	Connect to CN1 PIN1	Connect to power
3	NC	NC	4	GND	Ground
5	GND	Ground			

2.3 GPIO

The GPIO is a 40-pin header connector. The pins can be defined as

- Data input / output.
- Interrupt generation.



Pin	Signal	Description	Pin	Signal	Description
1	GND	Ground	2	GND	Ground
3	NC	NC	4	NC	NC
5	VDD33V	3.3V voltage	6	VDD33V	3.3V voltage
7	EINT4	Interrupt 4	8	EINT3	Interrupt 3
9	EINT8	Interrupt 8	10	EINT11	Interrupt 11
11	EINT14	Interrupt 14	12	EINT13	Interrupt 13
13	EINT19	Interrupt 19	14	EINT15	Interrupt 15
15	EINT18	Interrupt 18	16	EINT9	Interrupt 9
17	TOUT1	PWM out 1	18	TOUT0	PWM out 0
19	AIN2	AIN2	20	AIN3	AIN3
21	AIN0	AIN0	22	AIN1	AIN1

23	nSS_SPI	Nss_SPI	24	SPIMISO	SPI Master data in; slave data out
25	SPIMOSI	SPI Master data out; slave data in	26	SPICLK	SPI clock
27	GPG13	Gate Pulse Generator 13	28	GPG14	Gate Pulse Generator 14
29	nLED_3	nLED_3	30	nLED_4	nLED_4
31	nLED_1	nLED_1	32	nLED_2	nLED_2
33	VDD5V	5V voltage	34	VDD5V	5V voltage
35	I2CSDA	I2C data	36	I2CSCL	I2C clock
37	GND	Ground	38	GND	Ground
39	OM0	NAND/NOR Flash select	40	GND	Ground

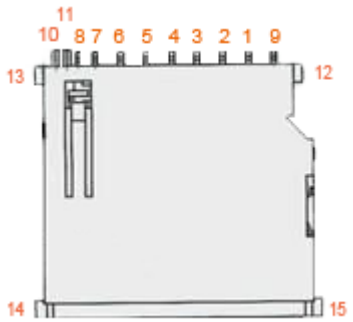
2.4 SD Card (CON7)

The SD card is used as an external storage device. It also can be used for booting card and download image. The MMC controller interface supports up to 4-bit transfer modes. MMC is always accessible through the carrier board interface.

Features:

- Low voltage consumption.
- Support hot-plug.
- Support SD mode and SPI mode.

CON7



Pin	Signal	Description	Pin	Signal	Description
1	SDDATA3	Card data bit 3	2	SDCMD	Command signal
3	GND	Ground	4	VDD33V	Power Positive 3.3V
5	SD_CLK	Interface clock	6	GND	Ground
7	SDDATA0	Card data bit 0	8	SDDATA1	Card data bit 1
9	SDDATA2	Card data bit 2	10	WP_SD_1	Test card is inserted
11	nCD_SD	Card write protect detection	12	GND	Ground
13	GND	Ground	14	GND	Ground
15	GND	Ground			

2.5 LCD (40P FPC, 50P Header)

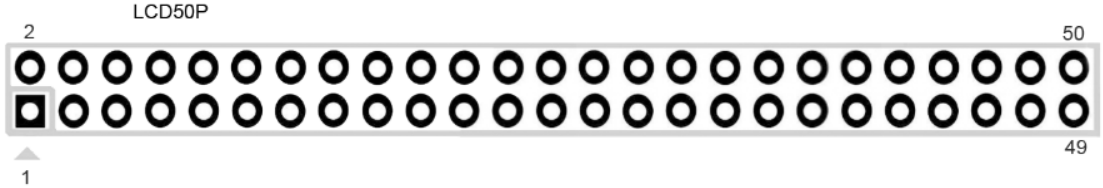
There are two LCD interfaces of EM2416, one is 40P FPC and the other is 50P header.

The board comes with driver for 4.3", 7" resistive LCD, user also can choose other size of LCD&touchscreen.

40Pin LCD connector

Pin	Signal	Pin	Signal
1	VDD_IN	2	VDD_IN
3	VD0	4	VD1
5	VD2	6	VD3
7	VD4	8	VD5
9	VD6	10	VD7
11	GND	12	VD8
13	VD9	14	VD10
15	VD11	16	VD12
17	VD13	18	VD14
19	VD15	20	GND
21	VD16	22	VD17
23	VD18	24	VD19
25	VD20	26	VD21
27	VD22	28	VD23
29	GND	30	LCD_PWR
31	I2CSDA	32	I2CSCL
33	VM	34	VFRAME
35	VLINE	36	VCLK
37	TSXM	38	TSXP
39	TSYM	40	TSYP

50Pin LCD connector



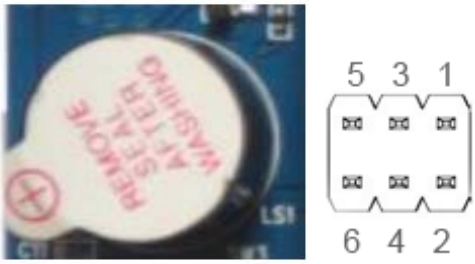
Pin	Signal	Pin	Signal
1	VDD _N	2	VDD _N
3	VDD _N	4	GND

5	NC	6	VD0
7	VD1	8	VD2
9	VD3	10	VD4
11	VD5	12	VD6
13	VD7	14	VD8
15	VD9	16	VD10
17	VD11	18	GND
19	VD12	20	VD13
21	VD14	22	VD15
23	VD16	24	VD17
25	VD18	26	VD19
27	VD20	28	VD21
29	VD22	30	VD23
31	GND	32	LCD_PWR
33	I2CSDA	34	I2CSCL
35	NC	36	VM
37	VFRAME	38	VLINE
39	VCLK	40	NC
41	NC	42	GND
43	TSXM	44	TSXP
45	NC	46	GND
47	TSYM	48	TSYP
49	NC	50	GND

2.6 PWM (J4)

The buzzer is active and will sound when a DC voltage is applied. Connect Pin 5 and 6 with Jumper to control PWM out.

BUZZER **J4**



J4

Pin1&3，Pin1&2 is used to power LCD (just control the backlight). The default select Pin1&3.

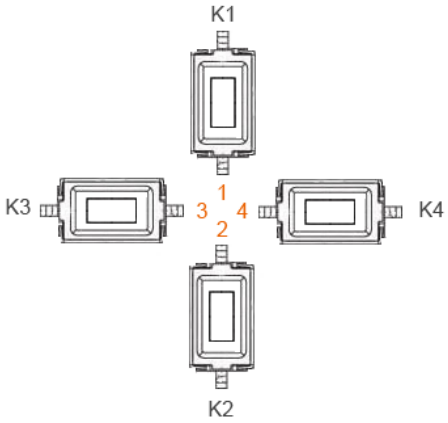
Pin	Signal	Description	Pin	Signal	Description
1	VDD_IN	Voltage in	2	VDD33V	3.3V voltage
3	VDD5V	5V voltage	4	NC	NC
5	NetJ4_5	Connect to buzzer	6	VDD5V	5V voltage

BUZZER

Pin	Signal	Description	Pin	Signal	Description
1	NetJ4_5	Voltage in. Connect to pin 5 of J4	2	NetU8_C	Control PWM. Connect to U3_ Collector

2.7 Buttons (K1/2/3/4, RST)

On-board 4 user buttons (User-Defined) and 1 reset button. User buttons by using polling program or interrupt the user can be acquainted with the voltage change of GPIO pins.



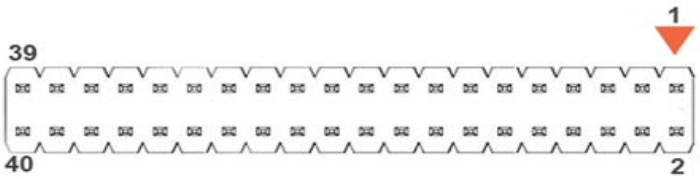
Pin	Signal	Description	Pin	Signal	Description
1	EINT1	Interrupt 1	2	EINT2	Interrupt 2
3	EINT3	Interrupt 3	4	EINT4	Interrupt 4
K1	GND	Ground	K2	GND	Ground
K3	GND	Ground	K4	GND	Ground

The RST button is a Side Tact Switch. The board adopts MAX811 as the Reset chip. If the system voltage is lower than the threshold 2.93V, MAX811S resets the system immediately.

Pin	Signal	Description	Pin	Signal	Description
1	NetU_3(MR)	Connect to MAX811	2	GND	Ground

2.8 BUS (J1)

The BUS is a 40pin header connector. There are 7 address lines (address 1-6 and address 25), 16 data lines (data 0-15), 2 interrupts and 4 chip select signals. The Bus supports IDE protocol and can be extended functionality.



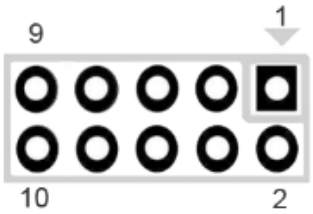
Pin	Signal	Description	Pin	Signal	Description
1	LADDR1	address 1	2	GPA0	GPIO port
3	LADDR3	address 3	4	LADDR2	address 2
5	LADDR5	address 5	6	LADDR4	address 4
7	LADDR25	address 25	8	LADDR6	address 6
89	LDATA6	Data 6	10	LDATA7	Data 7
11	LDATA4	Data 4	12	LDATA5	Data 5
13	LDATA2	Data 2	14	LDATA3	Data 3
15	LDATA0	Data 0	16	LDATA1	Data 1
17	nRESET	Bus reset	18	nWAIT	Request an extension of the current bus cycle
19	LnOE	Read Enable	20	LnWE	Write Enable
21	LDATA14	Data 14	22	LDATA15	Data 15
23	LDATA12	Data 12	24	LDATA13	Data 13
25	LDATA10	Data 10	26	LDATA11	Data 11
27	LDATA8	Data 8	28	LDATA9	Data 9
29	nGCS3	chip select signal 3	30	nGCS5	chip select signal 5
31	nIDE_CS1	chip select signal 1	32	nIDE_CS2	chip select signal 2
33	RXD3	Data receive 3	34	CTSn1	LnOE
35	EINT6	Interrupt 6	36	EINT5	Interrupt 5
37	GPB9	GPIO port	38	GPB10	GPIO port
39	VDD5V	5V voltage	40	GND	Ground

2.9 JTAG

The JTAG is a 2mm pitch 10-pin connector.

Features

- IEEE P1149.1, 1149.6 (standard JTAG) interface to off-chip test and development equipment
- Debug-related control and status



Pin	Signal	Description	Pin	Signal	Description
1	VDD33V	3.3V voltage	2	VDD33V	3.3V voltage
3	nTRST	Test logic reset	4	nRESET	Test logic reset
5	TDI	Test data input	6	TDO	Test data output
7	TMS	Test mode select	8	GND	Ground
9	TCK	Test clock	10	GND	Ground

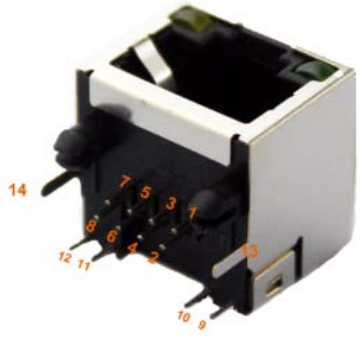
2.10 Ethernet (LAN_100M)

EM2416 incorporates a full-featured 10/100M Ethernet interface. The platform adopts DM9000CEP as the Ethernet chip.

Features:

- 10/100 BASE-T IEEE 802.3 compliant
- IEEE 802.3u compliant Auto-Negotiation
- Integrated IEEE 1588 time stamping module (inside the MAC).
- Automatic channel swap (ACS)
- Full- and Half-duplex
- Automatic MDI/MDIX crossover
- Automatic polarity correction
- Activity and speed indicator LED controls

The EM2416 can download the images by TFTP server via Ethernet.



Pin	Signal	Description	Pin	Signal	Description
1	TD+	Data send +	2	TD-	Data send -
3	RD+	Data receive +	4	AGND	Ground
5	AGND	Ground	6	RD-	Data receive -
7	AGND	Ground	8	AGND	Ground
9	LAND2_LNK	Detect link	10	VDD33V	3.3V voltage

11	LAND2_SPD	Detect speed	12	VDD33V	3.3V voltage
13	GND	Ground	14	GND	Ground

2.11 Serial port (COM1, UART)

On-board a single RS232 port.

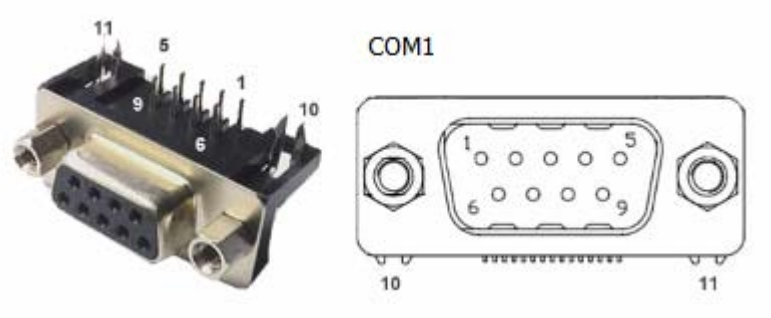
Features:

• 32-entry FIFO for receiver and 32-entry FIFO for transmitter

• Programmable baud rate of up to 250K bit/s

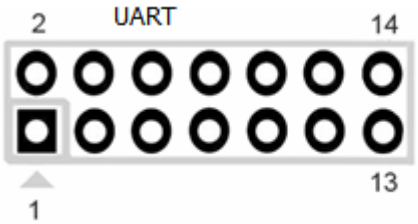
• The serial port operates at RS232 voltage levels.

The RS232 is used for debugging. It is used to input and display interactive command, view system boot information and transfer files.



Pin	Signal	Description	Pin	Signal	Description
1	NC	NC	2	RSTXD0	RS232 serial data out
3	RSRXD0	RS232 serial data in	4	NC	NC
5	GND	Ground	6	NC	NC
7	RSCTS0	Clear to Send	8	RSRTS0	Request to Send
9	NC	NC	10	GND	Ground
11	GND	Ground	12		

The UART is a 14-pin connector and can be extended to 3x three-wire serial ports.



Pin	Signal	Description	Pin	Signal	Description
1	GND	Ground	2	GND	Ground
3	VDD5V	5V voltage	4	VDD5V	5V voltage
5	TXD2	Serial 2 data out	6	RXD2	Serial2 data in
7	TXD1	Serial 1 data out	8	RXD1	Serial1 data in

9	TXD0	Serial 1 data out	10	RXD0	Serial0 data in
11	nCTS0	Clear to Send 0	12	nRTS0	Request to Send 0
13	VDD33V	3.3V voltage	14	VDD33V	3.3V voltage

2.12 USB2.0 Device (USB_DEVICE)

The USB2.0 device is a type-B USB.

Features:

• Supports USB 2.0 High Speed (480Mbps), Full Speed (12Mbps) and Low Speed (1.5Mbps) operation in host mode

• Supports USB 2.0 High Speed (480 Mbps) and Full Speed (12 Mbps) operation in peripheral mode.

Pin	Signal	Description	Pin	Signal	Description
1	VBUS	USB_5V	2	D-	DND1/GND
3	D+	DPD1/USB_EN/EINT2	4	GND	Ground
5	GND	Ground	6	GND	Ground

2.13 USB1.1 HOST (USBH)

USBH is a type A Double-USB1.1 Host. It supports full speed (12Mbps) and low speed (1.5Mbps) operation. It can be used to connect USB mouse, U disk and other USB devices.

Pin	Signal	Description	Pin	Signal	Description
1	VDD5V	3.3V voltage	2	DN0	USB host data 0(-)
3	DP0	USB host data 0(+)	4	GND	Ground
5	VDD5V	3.3V voltage	6	DNH1	USB host data 1(-)
7	DPH1	USB host data 0(+)	8	GND	Ground
9	GND	Ground	10	GND	Ground
11	GND	Ground	12	GND	Ground

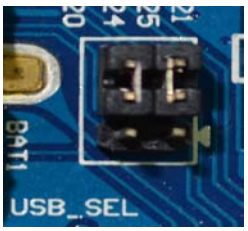
There are three USB ports (one USB device and two USB host), but only two USB are active at one time. The USB can be specified by operating the USB_SEL.

Pin	Signal	Description	Pin	Signal	Description
1	DND1	USB device data1(-)	2	DPD1	USB device data1(+)
3	DN1	USB host data 1(-)	4	DP1	USB host data 1(+)
5	DNH1	USB host data 1(-)	6	DPH1	USB host data 0(+)

How to select USB:

1. USB device and Bottom_USB host are active. (connect pin1&3, pin2&4)

2. Double-USB host are active (Bottom_USB host and Top_USB host). (connect pin3&5, pin4&6)



2.14 Audio I/O (MIC, PHONE)

The development board adopts IIS interface chip WM8731, provides stereo audio output (Green, 3.5mm audio jack) and MIC recording (Pink, 33.5mm audio jack).

Features:

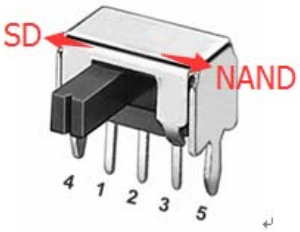
- Low power
- Integrated ADC and DAC
- IIS transfer audio data

- Stereo output, support recording

MIC					
Pin	Signal	Description	Pin	Signal	Description
1	GND	Ground	2	MICIN	MIC input
3	MICIN	MIC input	10	MICIN	MIC input
11	MICIN	MIC input			
PHONE					
Pin	Signal	Description	Pin	Signal	Description
1	GND	Ground	2	LHPOUT	Left Channel Headphone Output
3	RHPOUT	Right Channel Headphone Output	10	LHPOUT	Left Channel Headphone Output
11	RHPOUT	Right Channel Headphone Output			

2.15 Boot Switch (F_SEL)

F_SEL is a Toggle Switch used to set the boot mode. If turn to the left, it is booting from SD Card, otherwise, bootting from NAND.



Pin	Signal	Description	Pin	Signal	Description
1	NC	NC	2	OM0	Boot select
3	GND	Ground	4	GND	Ground
5	GND	Ground			

2.16 Backup battery (BAT1)

The backup battery (3V) is used to ensure that the RTC (frequency 32.768KHz) is still able to work after power off. Lithium cell model: CR1220.

3 Product Configurations